KPR Institute of Engineering and Technology

Learn Beyond

(Autonomous, NAAC "A")

Avinashi Road, Arasur, Coimbatore.

Phone: 0422-2635600
Web: kpriet.ac.in
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EE001

NBA Accredited
(CSE, ECE, EEE, MECH, CIVIL)

GUEST LECTURE ON "CHIP DESIGN AND ITS FUNDAMENTALS"

Event No	EE001
Organizing Department	Electrical and Electronics Engineering
Date	28/02/2024
Time	02:00 PM to 04:00 PM
Event Type	Guest Lecture
Event Level	Dept. Level
Venue	Thanam Hall
Total Participants	155
Faculty - Internal	5
Students - Internal	150

Related SDG

Resource Persons

SI	Type	Name	Designation	Company	Email	Phone
1	Resource Person	Dr K Saravanan	Design Engineer	Tech Mahindra, Bangalore	ksaravanan@gmail.com	xxxxxxxxxx

Involved Staffs

SI	Name	Role
1	Kamalkumar V	Coordinator
2	Revathi R	Coordinator

Outcome

Students learnt about the basics of VLSI, FINFET technology, Tools used, Job Roles and Placement Opportunities in core company.
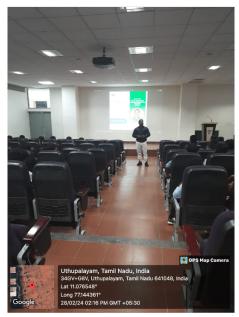
Event Summary

The Department of Electrical and Electronics Engineering organized Guest Lecture on "Chip Design and its Fundamentals" for the first year and third year students of EEE and ECE on 28/02/2024. A heart-warming welcome was delivered by Ms. R. Revathi, Assistant Professor (Sr.G) / EEE. Dr. K. Saravanan, Design Engineer/VLSI DESIGN, Tech Mahindra, Bangalore has been invited as a resource person to deliver the lecture on chip design and its fundamentals. He has shared his personal experience about how to integrate software and hardware using HDL and VLSI technology. He shared the data sheet and software tools available in VLSI. He also delivered the applications of logic gates in electronic circuits. He briefly explained about the CMOS technology. He also shared his experience and asked the students to upgrade the skills based on the company profile. During the session, he shared the job roles and opportunities in core industry. Students interacted well and asked queries during the session. The session was an eye opening for all the participants. The session ended with vote of thanks which was delivered by Mr. V. Kamalkumar, Assistant Professor (Sr.G) / EEE.

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